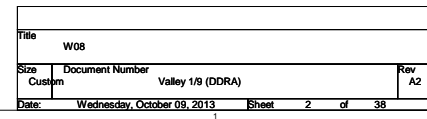
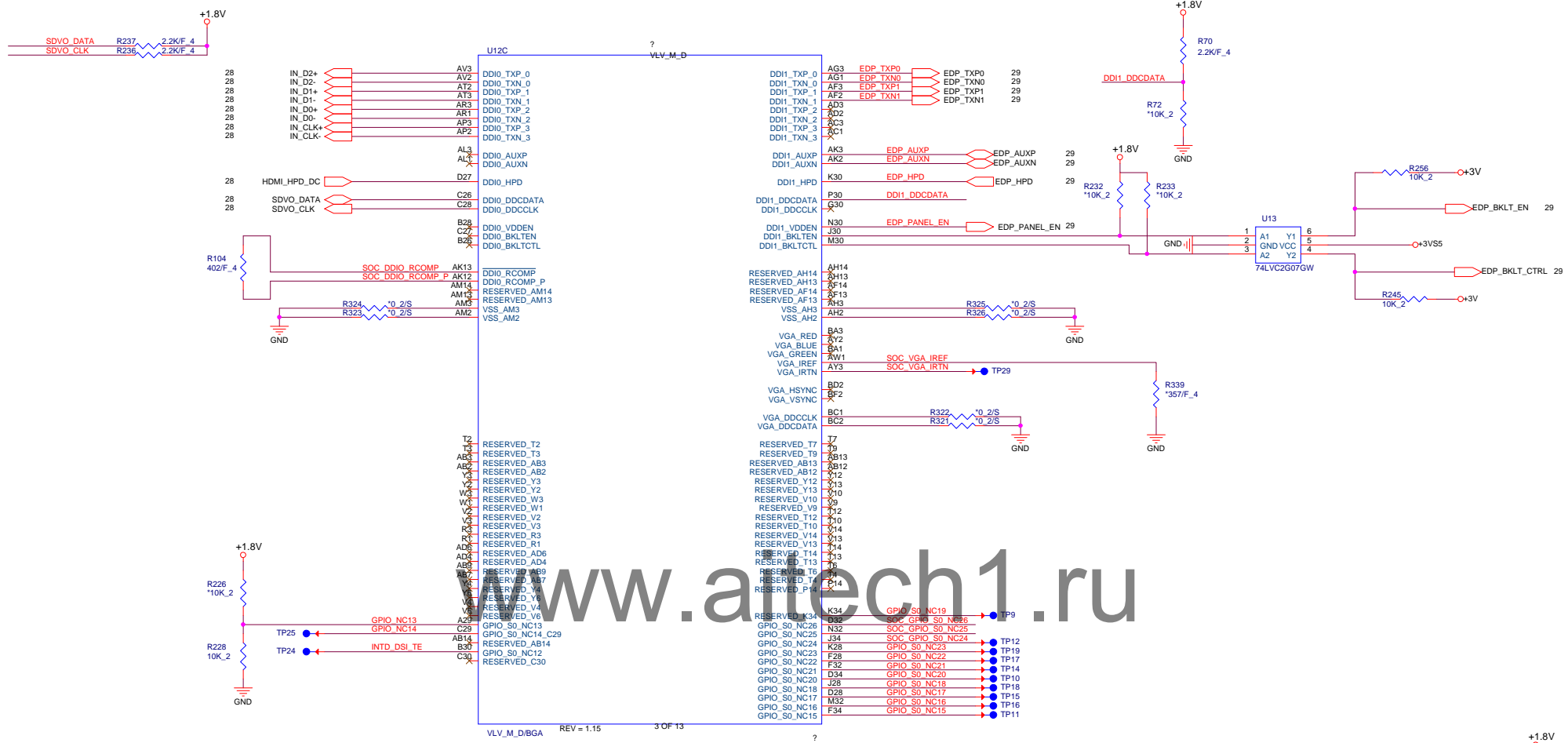


Slate



Date: Tuesday, October 08, 2013 Sheet 1 of 38





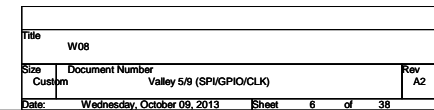
Layout notes: Move to bottom side

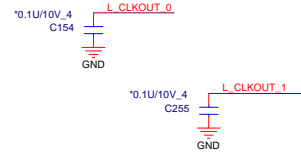
	GPIO26	GPIO25	GPIO24	PN
Hynix (TG) H5TC4G63AFR-PBA/4Gb X16/DC(4GB)	0	0	0	AKD5JGETW00
Micron MT41K256M16HA-125:E/4Gb x16/DC(4GB)	0	0	1	AKD5JGSTL02
Hynix (TG) H5TC4G63AFR-PBA/4Gb X16/SC(2GB)	0	1	0	AKD5JGETW00
Micron MT41K256M16HA-125:E/4Gb x16/SC(2GB)	0	1	1	AKD5JGSTL02
NA	1	0	0	NA

BOARD ID:0000

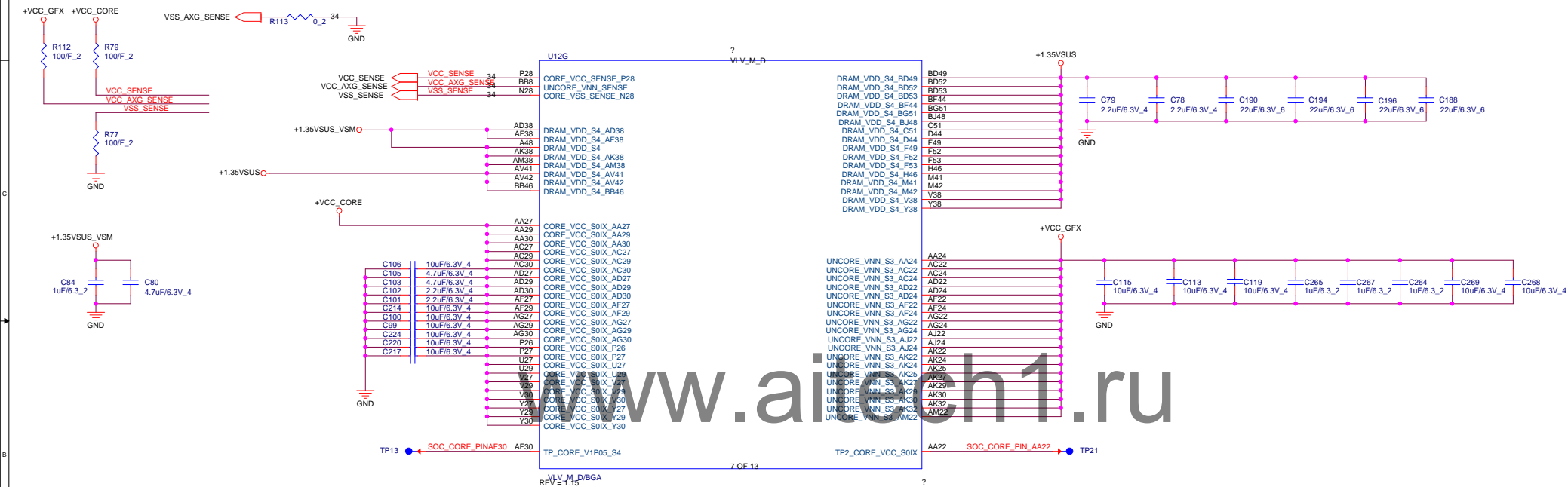
Title	W08	Rev	A2
Size	Custom	Document Number	Valley 3/9 (Display)
Date:	Wednesday, October 09, 2013	Sheet	4 of 38

A

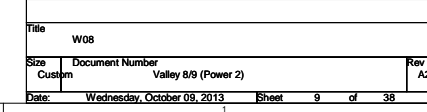
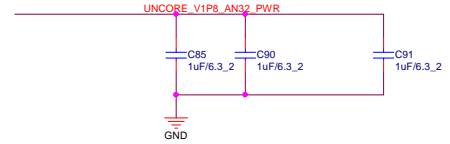
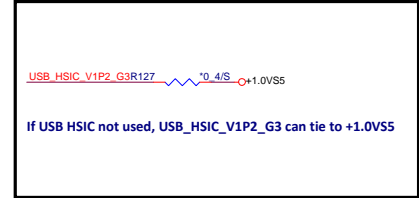


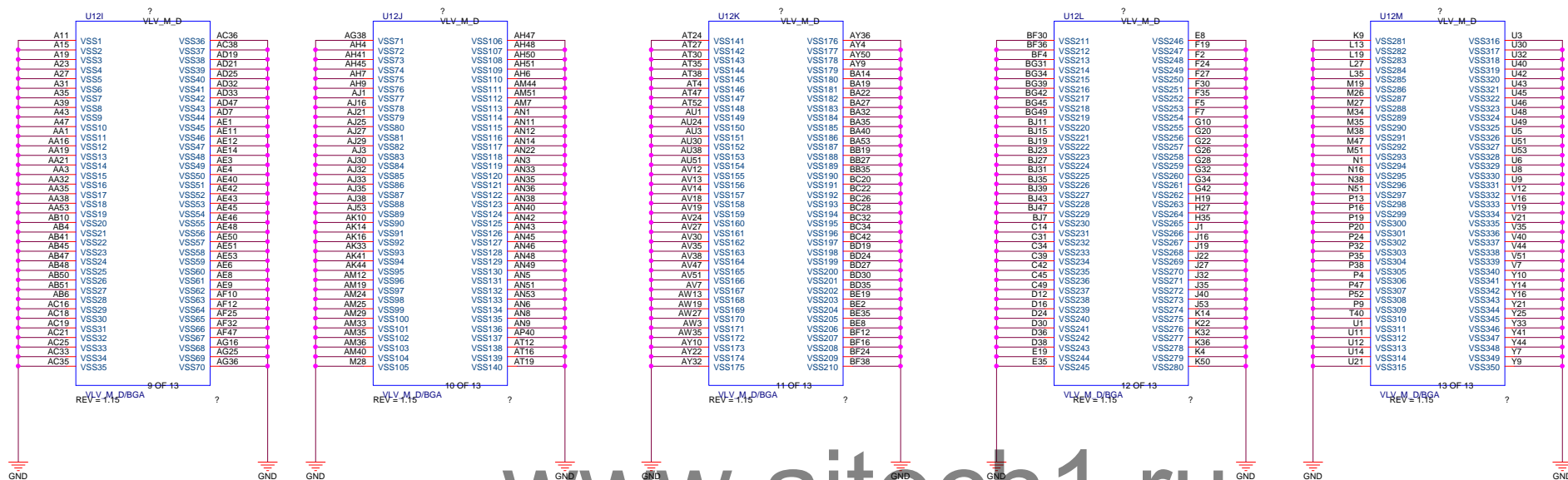


Title			
W08			
Size	Document Number		Rev
Custom	Valley 6/9 (USB/LPC/12C)		A2
Date:	Wednesday, October 09, 2013	Sheet	7 of 38

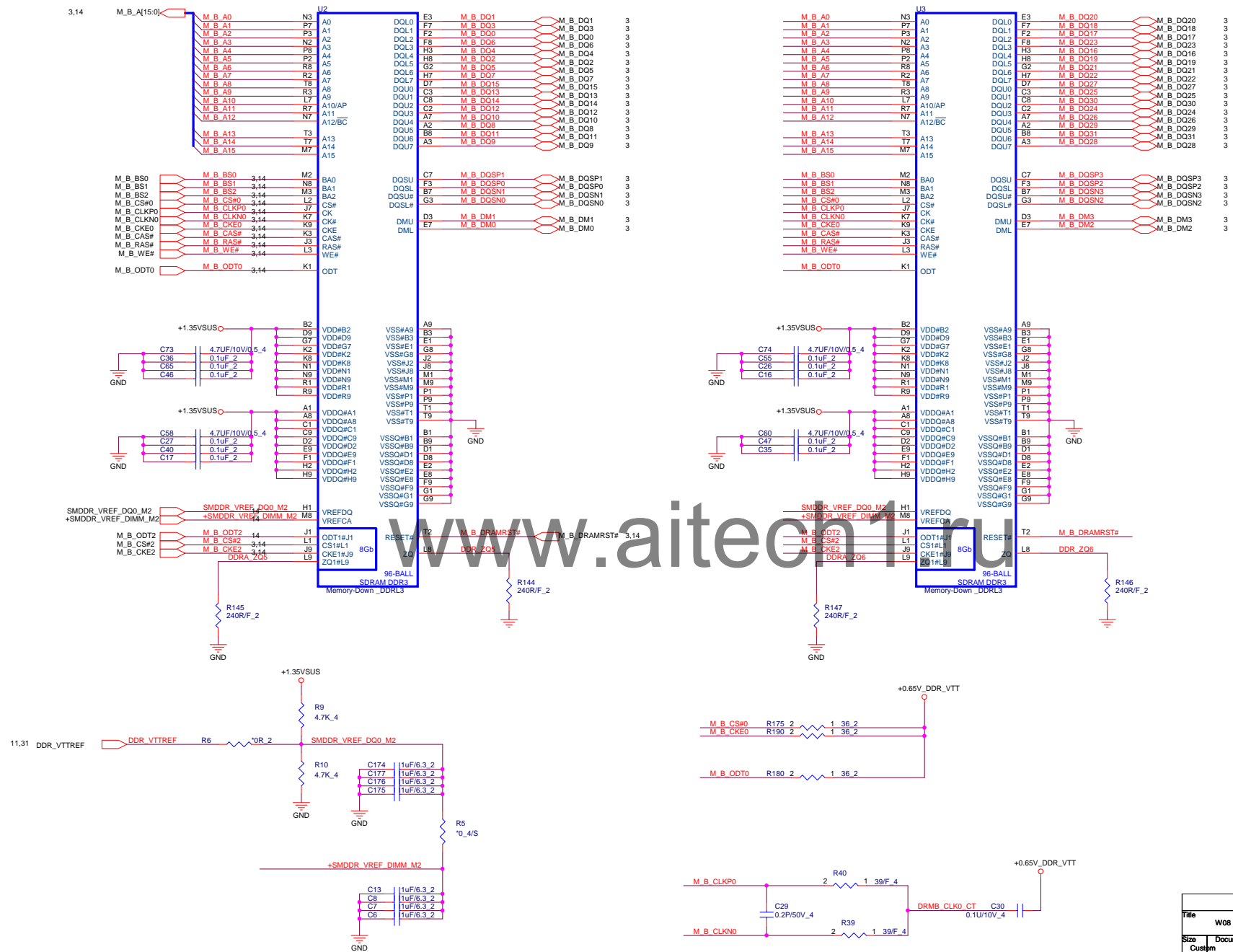


Title			W08
Size	Document Number	Valley 7/9 (Power 1)	
Custom		Rev A2	
Date:	Wednesday, October 09, 2013	Sheet	8 of 38

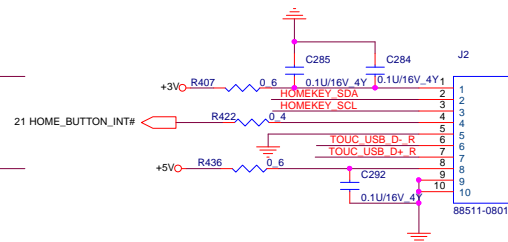
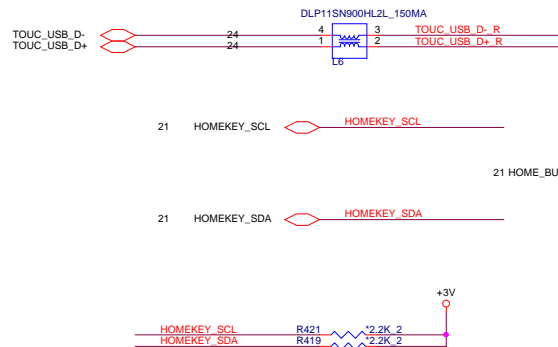
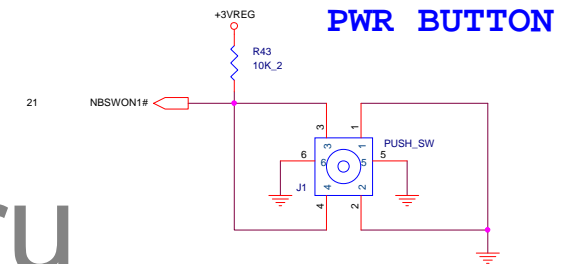
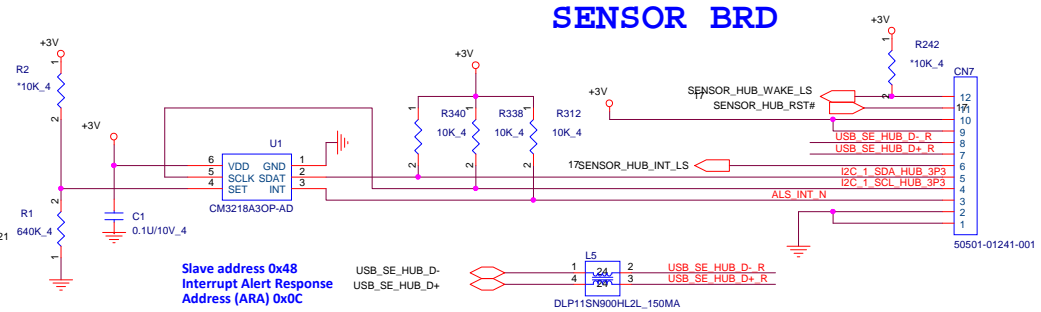
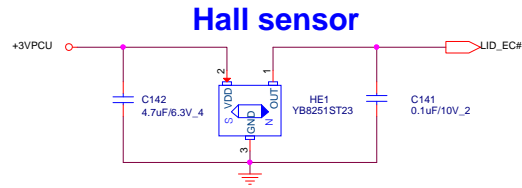




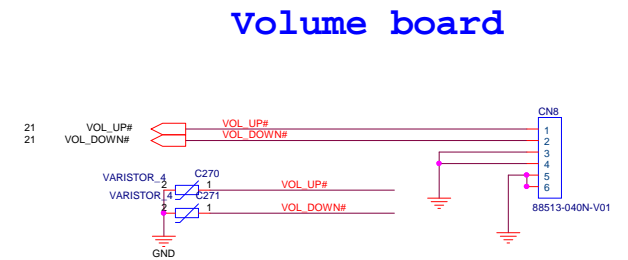




Title			W08
Size	Custom	Document Number	DDR3L - B1
Date:	Tuesday, October 08, 2013	Sheet	13 of 38
Rev			A2



DOWN edge contact at conn
UP/DOWN edge contact at touch board
Cable 折一次 (J4 pin define 已经reverse 一次)



Title			W08
Size	Document Number	Rev	
Custom	SMALL BOARD CONN	A2	
Date:	Friday, November 01, 2013	Sheet	15 of 38

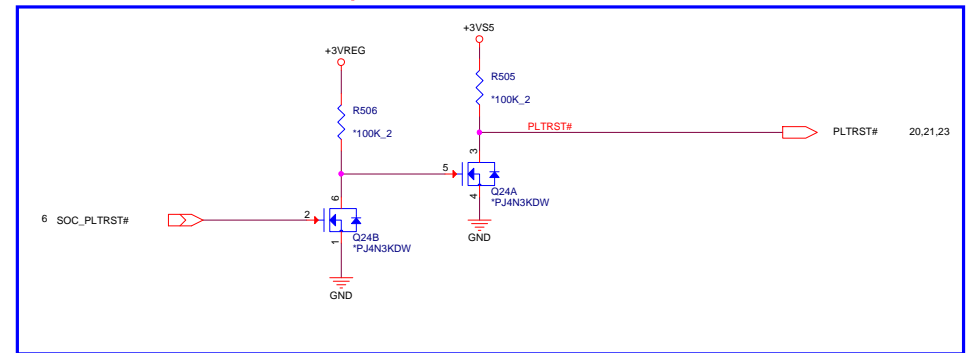
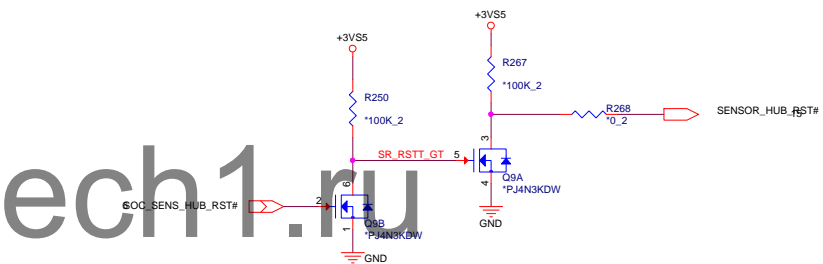
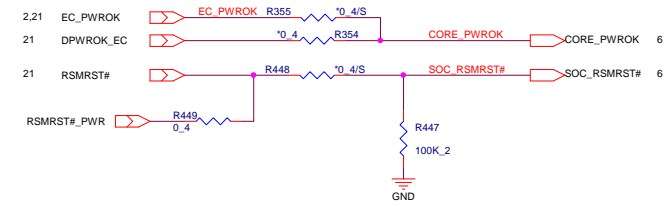
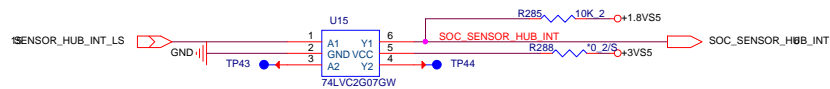
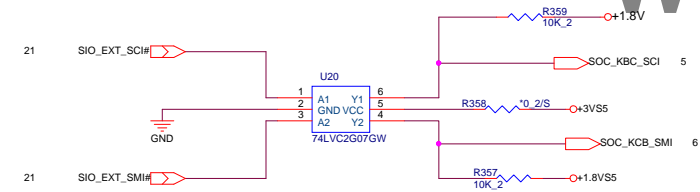
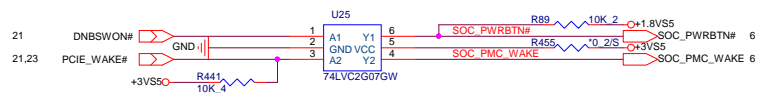
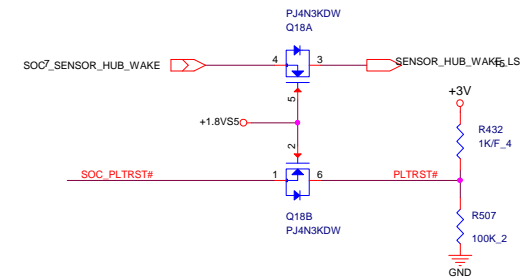
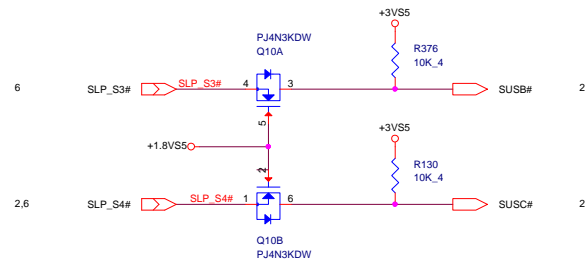
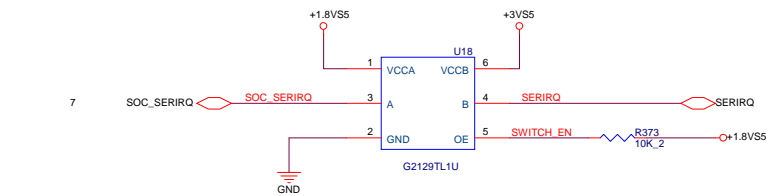
www.aitech1.ru

System assembly screw hole**Nut PN:MBW08001010**

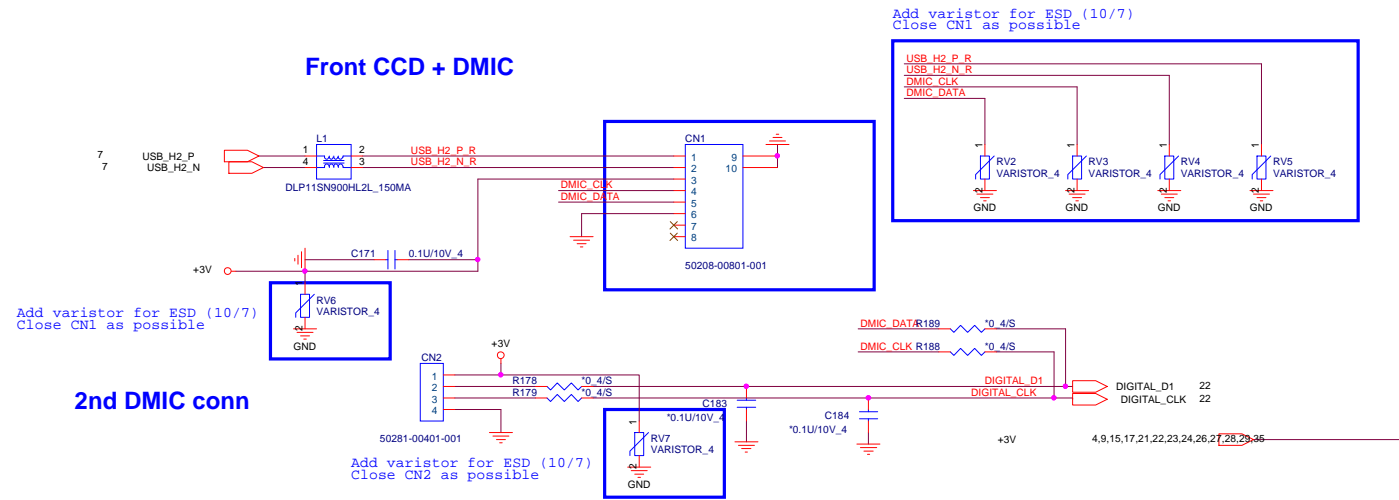
www.aitech1.ru

Heat pipe screw hole**Nut PN:MBW08002010**

Title			W08
Size	Document Number	Rev	
Custom	HOLE	A2	
Date:	Tuesday, October 08, 2013	Sheet	16 of 38



Title		W08
Size	Document Number	Rev
Custom	Level Shifter	A2
Date:	Tuesday, October 08, 2013	Sheet 17 of 38



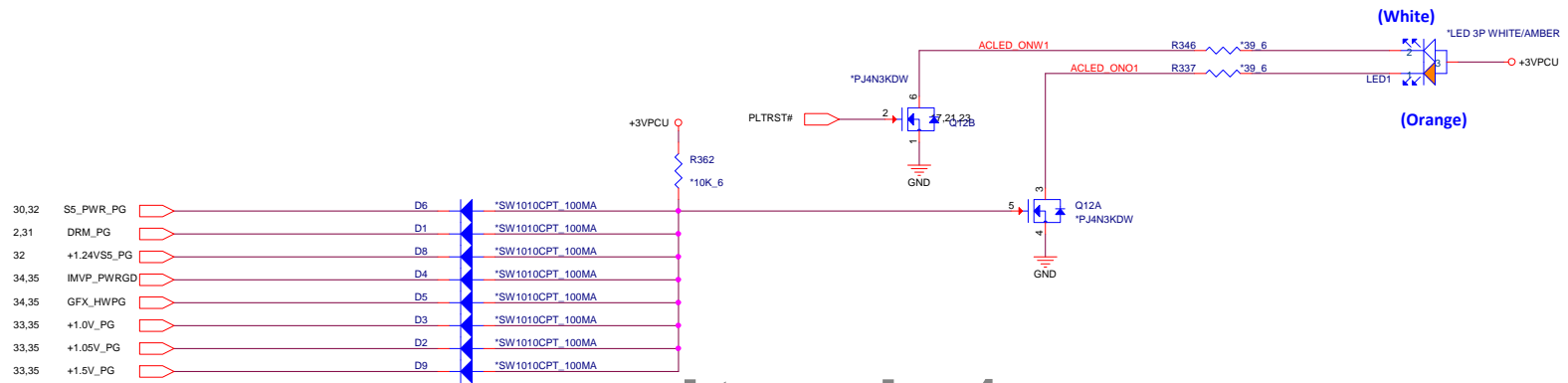
www.aitech1.ru

Title			W08
Size	Document Number	Rev	
Custom	Front/Rear Camera Module	A2	
Date:	Tuesday, October 08, 2013	Sheet	18 of 38

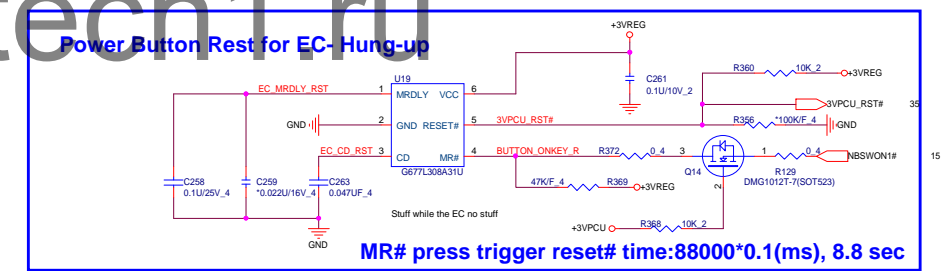
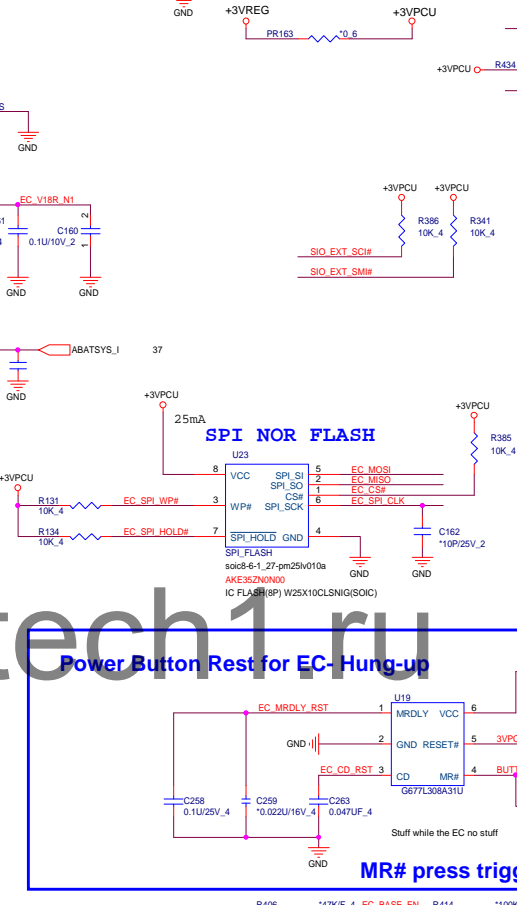
Table 36. Straps

Signal Name	Function	Default	Strap Exit	Strap Description
GPIO_S0_SC[056]	Legacy	1b	PMC_CORE_PWROK de-asserted	Top Swap (A16 Override) 0 = Top address bit is unchanged 1 = Top address bit is inverted
GPIO_S0_SC[063]	Legacy	1b	PMC_CORE_PWROK de-asserted	BIOS Boot Selection 0 = LPC 1 = SPI
GPIO_S0_SC[065]	Legacy	1b	PMC_CORE_PWROK de-asserted	Security Flash Descriptors 0 = Override 1 = Normal Operation
DDIO_DDCDATA	Display	0b	PMC_CORE_PWROK de-asserted	DDIO Detect 0 = DDIO not detected 1 = DDIO detected
DDI1_DDCDATA	Display	0b	PMC_CORE_PWROK de-asserted	DDI1 Detect 0 = DDI1 not detected 1 = DDI1 detected

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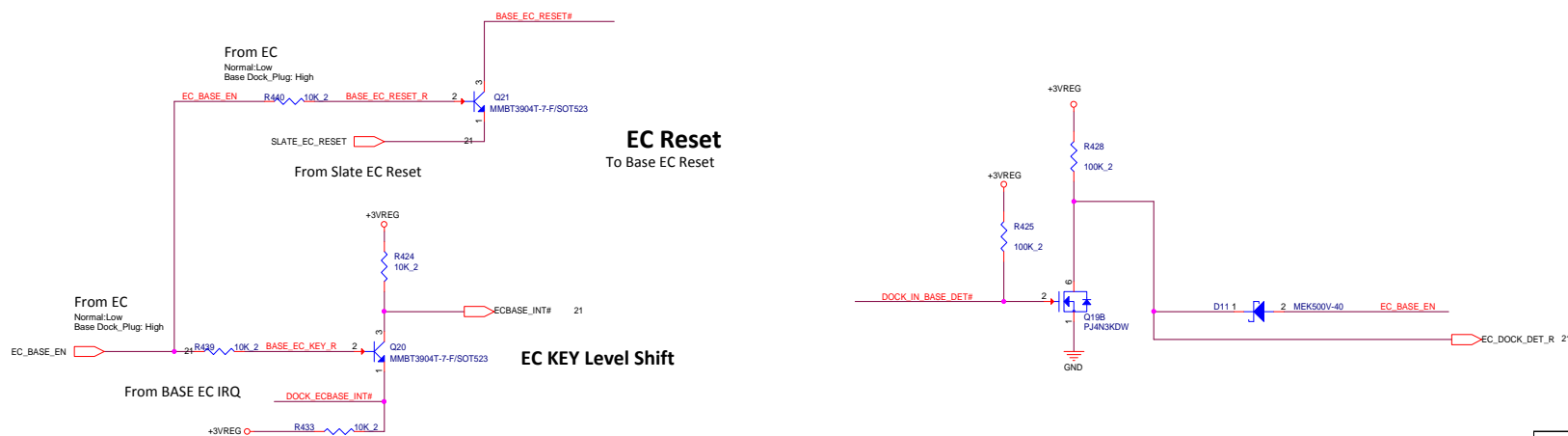


The schematic diagram illustrates the electrical connections for the EC board. Key components include:

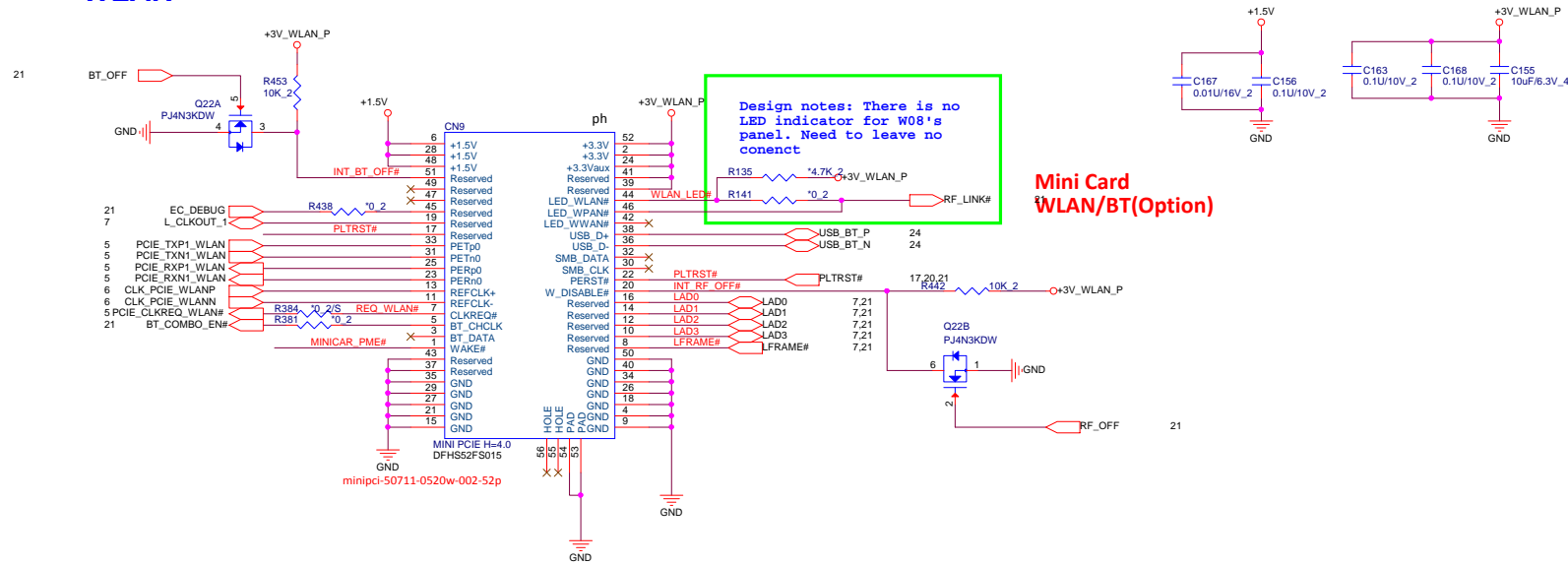
- Resistors:** R375 (10K_4), R406 (~47K/F_4), R414 (~100K_2), R138 (~47K_2), R139 (~47K_2), R466 (10KF_2), R417 (10K/F_2), R481 (~47K_2), R460 (~47K_2), R416 (10K/F_2), R415 (10K/F_2), R387 (~47K_2), R383 (~47K_2), R402 (~10K_2), R377 (~47K/F_4), R459 (~47K_2), R458 (~47K_2), R382.
- Capacitors:** C273 (0.1U/10V_2), C266 (0.1U/10V_2).
- Connectors and Headers:** BAT I2C_SDA, BAT I2C_SCL, DNBSSWONF#, EC_PCIE_WAKE#, ECBASE_DATA, VOL_DOWN#, VOL_UP#, LID_EC#, HOME_BUTTON_INT#, EN_OVERRIDE, H_PROCHOT#, ECBASE_CLK, ECBASE_DATA.
- Other Components:** Q11 (2N7002K), D11E, D11F, EGA10603V05A1-B.

The diagram shows the connection of these components to various power rails (+3VREG, +3VPCPU, +3VSS, +3VO) and ground. It also includes labels for specific functions like EC_LOCK#, HPGP, Test mode enable, and H_PROCHOT#.

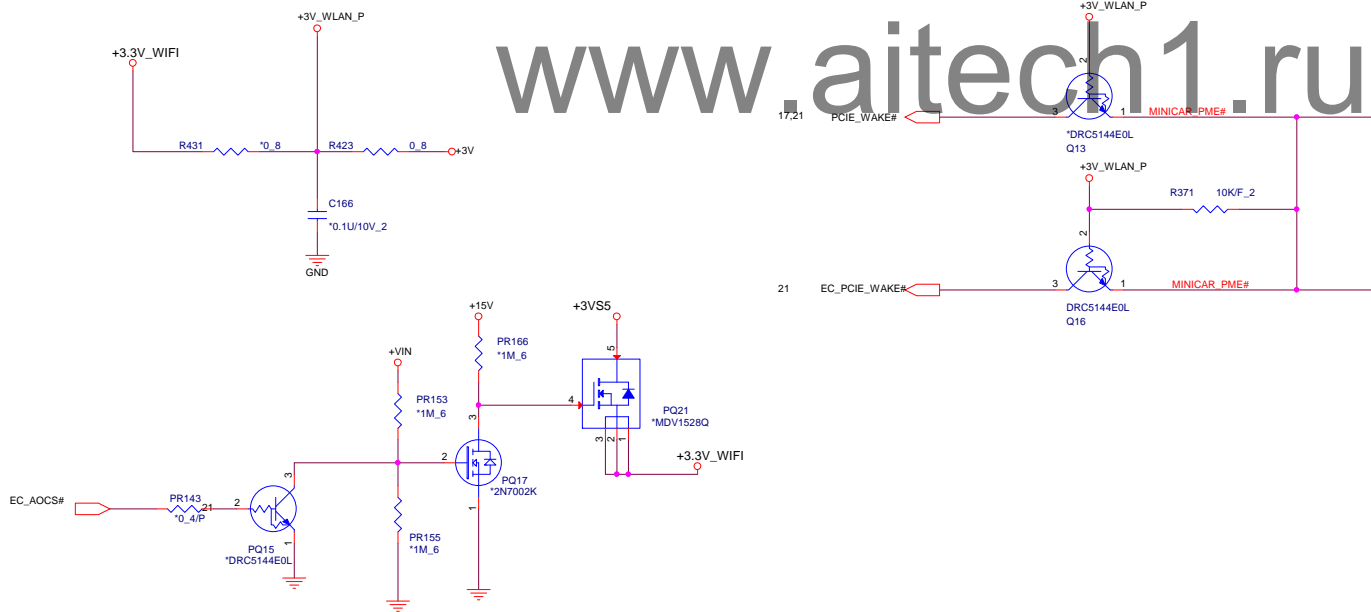
Title	
W06	
Size	Document Number
C	EC(IO9016)
Date:	Friday, November 01, 2013
Sheet	21 of 38



WLAN

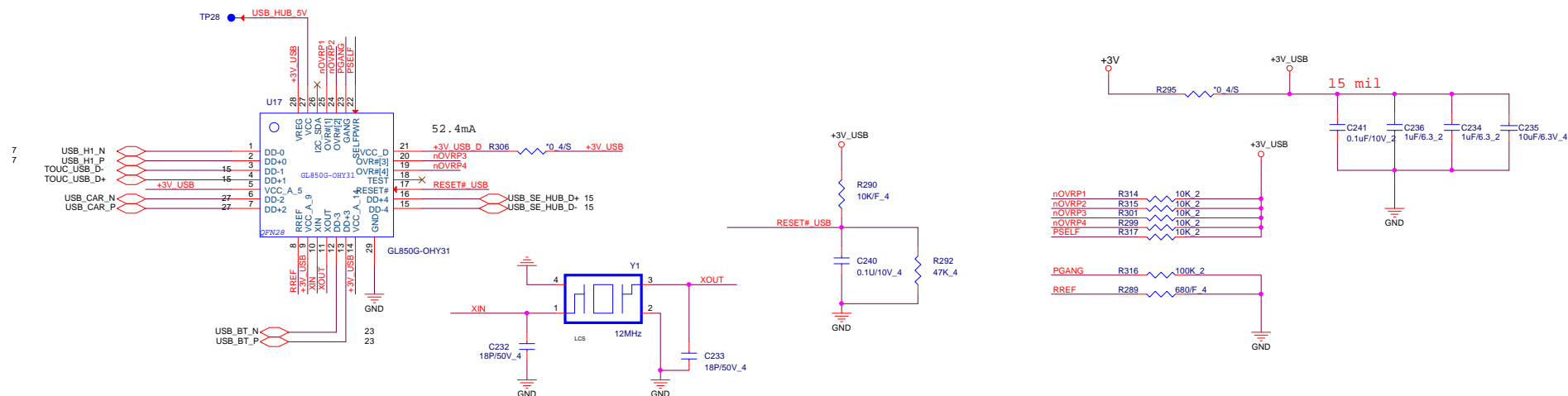


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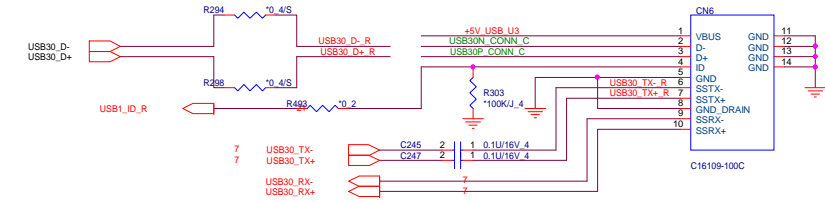
No support AOAC and Wake on WLAN

Title			W08
Size	Document Number	Rev	
Custom	WIFI & BT	A2	
Date:	Tuesday, October 08, 2013	Sheet	23 of 38

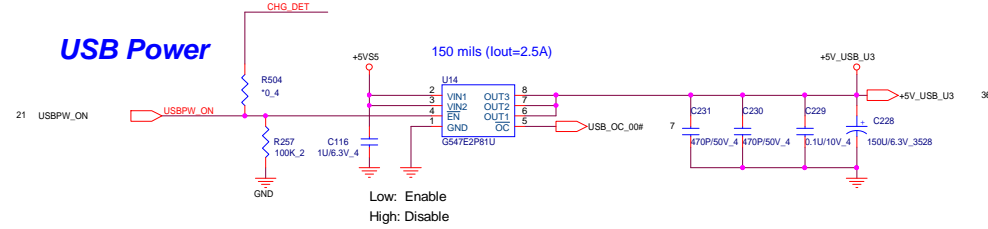


www.aitech1.ru

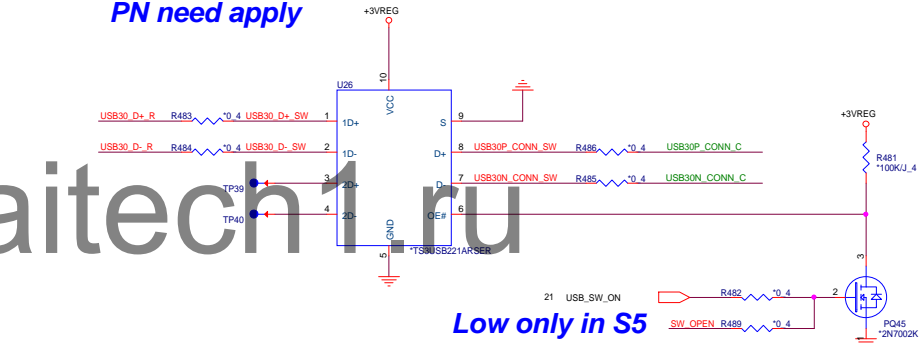
Micro-USB 3.0



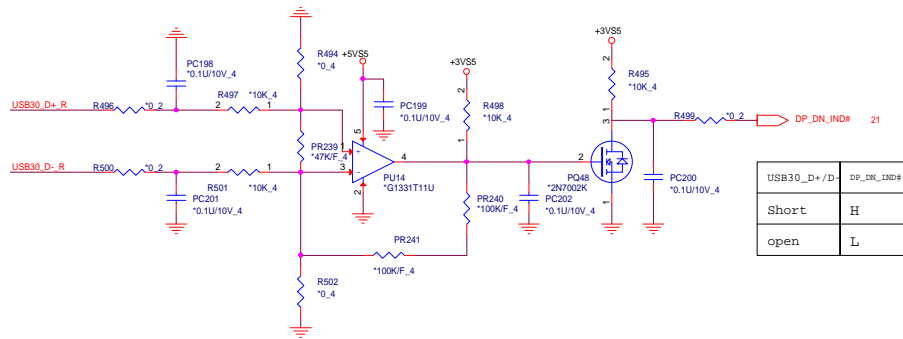
USB Power



PN need apply



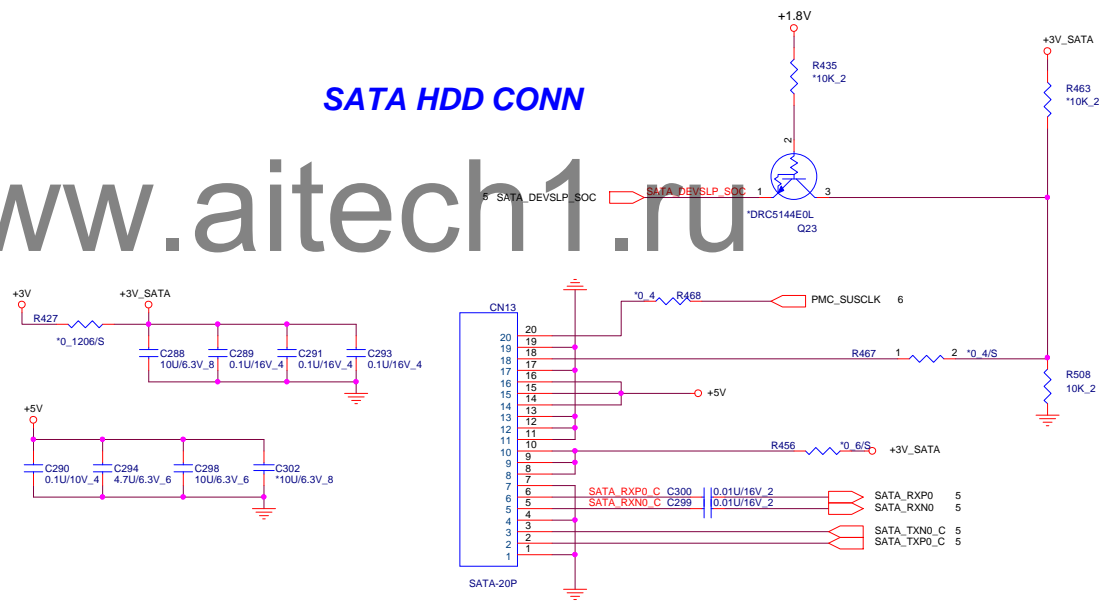
Low only in S5



USB30_D+ / D-	DP_DN_IND#
Short	H
open	L

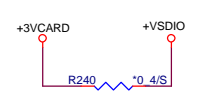
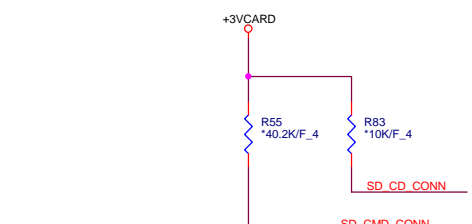
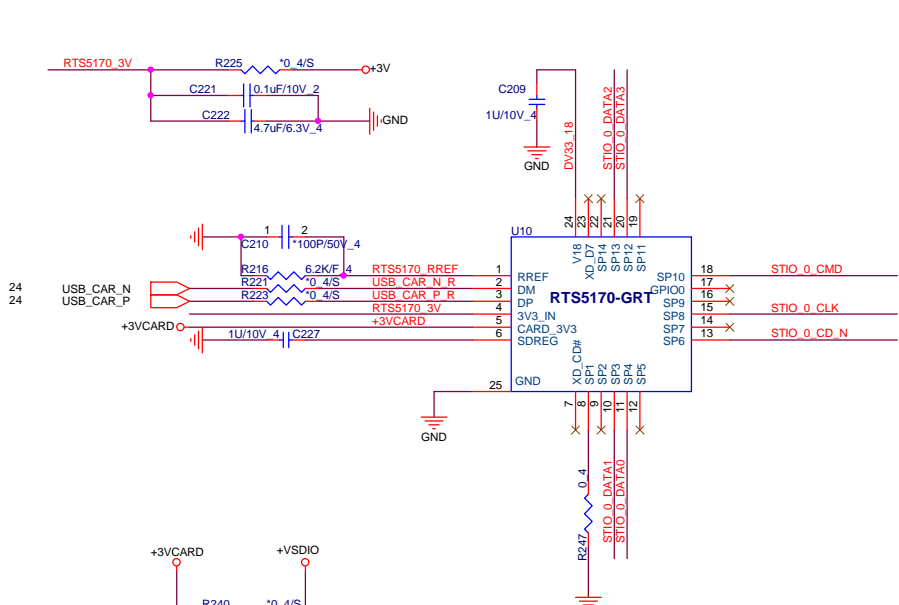
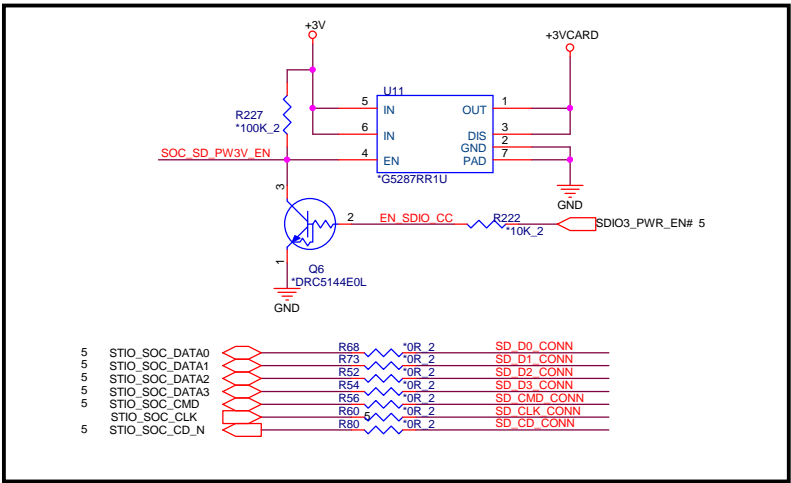
SATA HDD CONN

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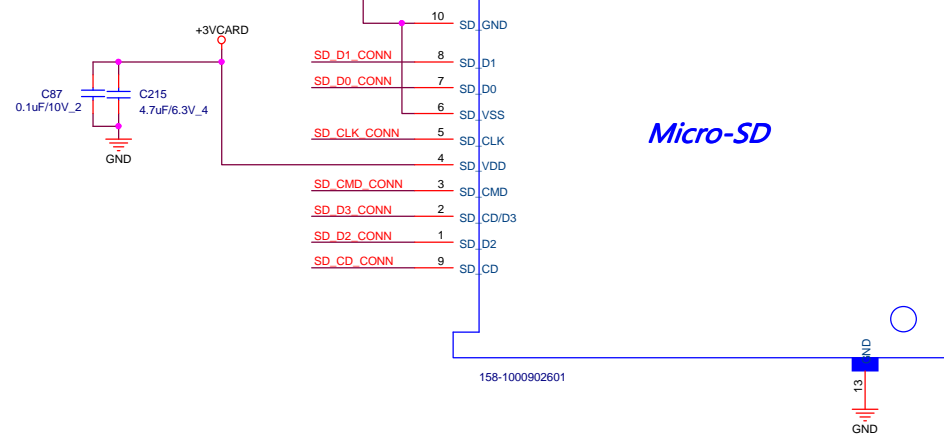
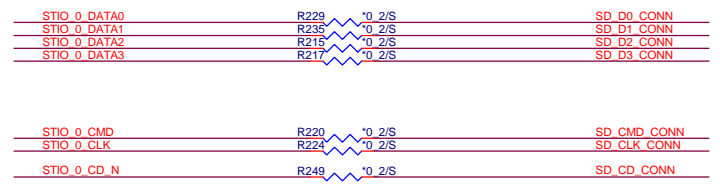
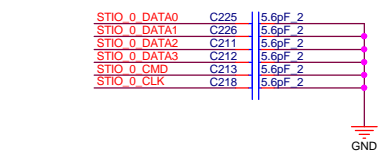


HDD CONN

Title			W08
Size	Custom	Document Number	SATA HDD
Date:	Tuesday, October 08, 2013	Sheet	26 of 38
		Rev	A2

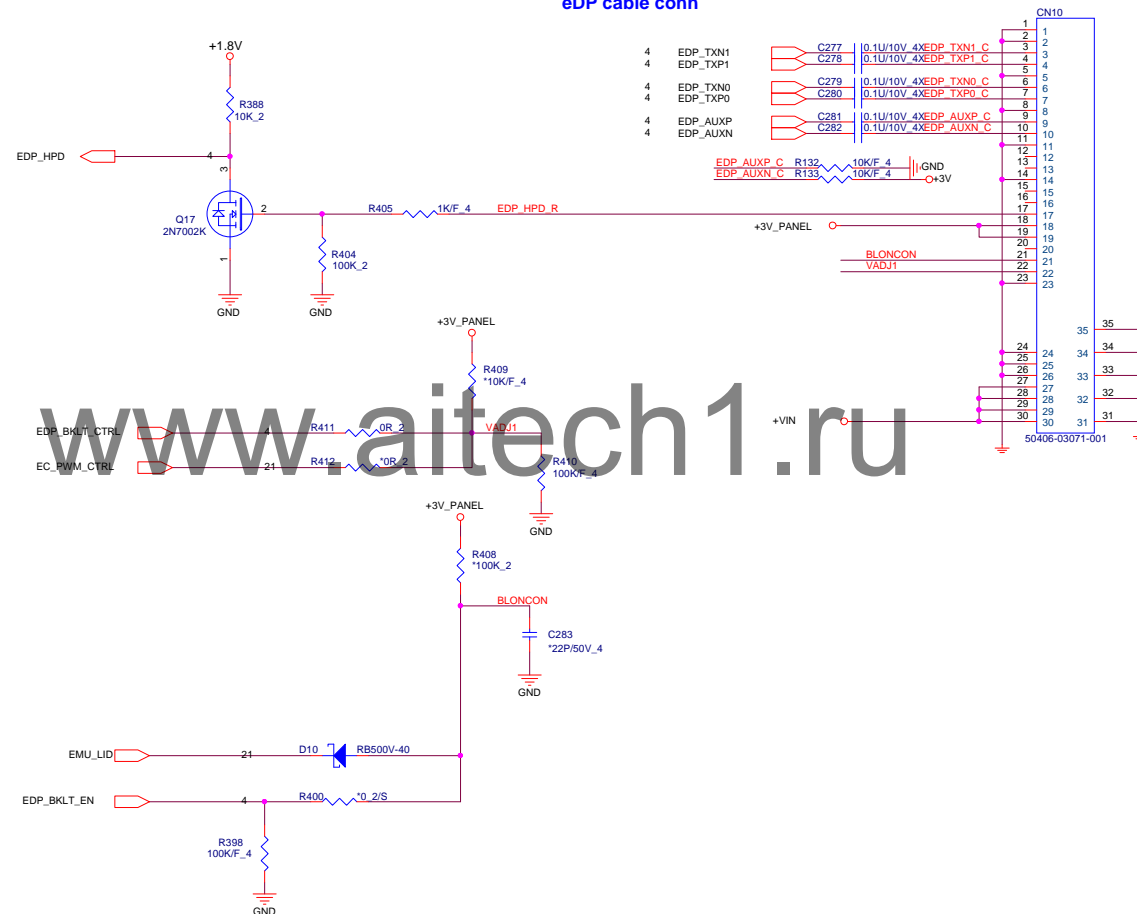


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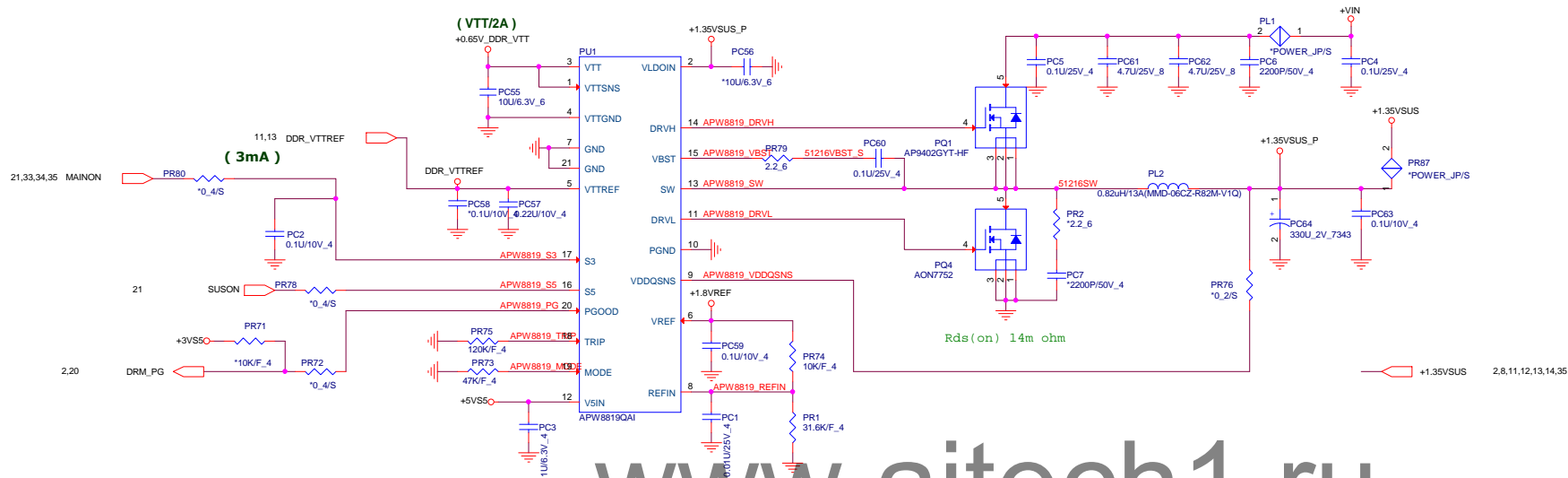
PIN	Signal Assignment	PIN	Signal Assignment
1	Hot Plug Detect	2	Utility
3	TMDS Data2+	4	TMDS Data2 Shield
5	TMDS Data2-	6	TMDS Data1+
7	TMDS Data1 Shield	8	TMDS Data1-
9	TMDS Data0+	10	TMDS Data0 Shield
11	TMDS Data0-	12	TMDS Clock+
13	TMDS Clock Shield	14	TMDS Clock-
15	CEC	16	DDC/CEC Ground
17	SCL	18	SDA
19	+5V Power		





Title			
W08			
Size	Document Number		Rev
Custom	eDP		A2
Date:	Tuesday, October 08, 2013	Sheet	29 of 38

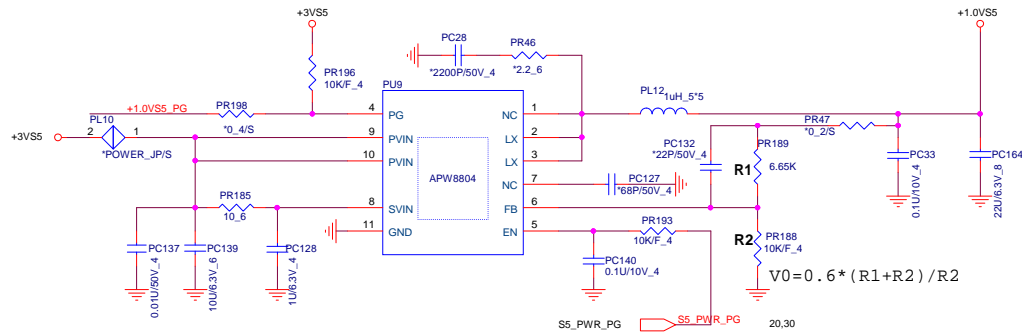
+Vin=6V~8.4V
+1.35VSUS Volt +/- 5%
Countinue current:6A
Peak current:6.4A
OCP minimum:9A



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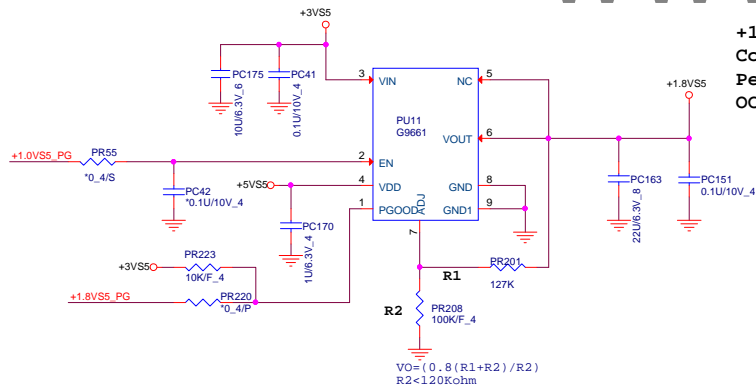
Title			W08
Size	Document Number	Rev	
Custom	DDR3(APW8819QAI)	A2	
Date:	Tuesday, October 08, 2013	Sheet	31 of 38

Countinue current:2A
Peak current:3A
OCP minimum 4.5A

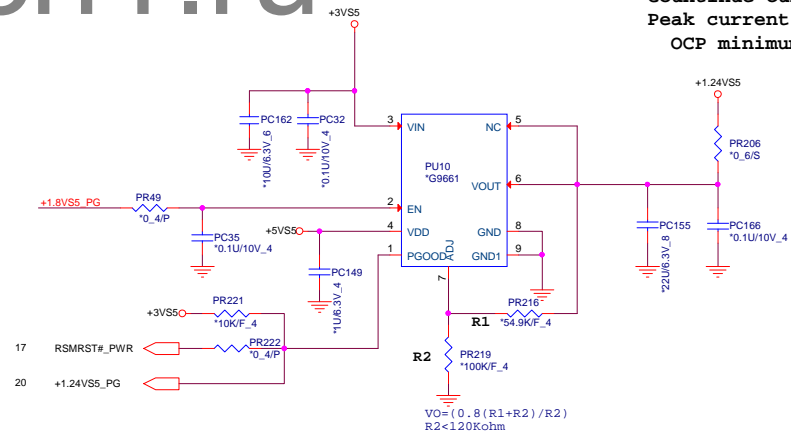


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+1.8V +/- 5%
Countinue current:0.1A
Peak current:0.1A
OCP minimum 0.5A

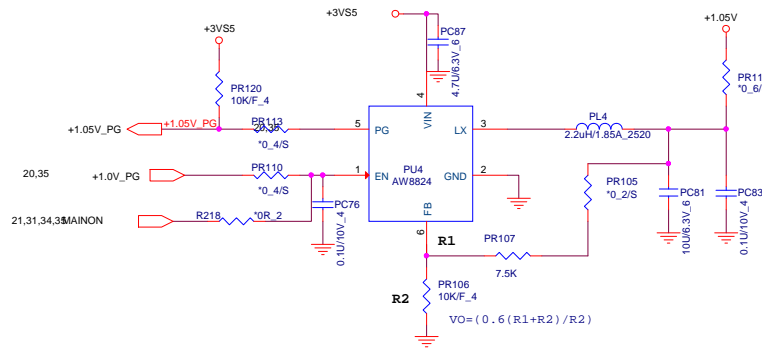


+1.24V +/- 5%
Countinue current:0.5A
Peak current:1A
OCP minimum 1.45A

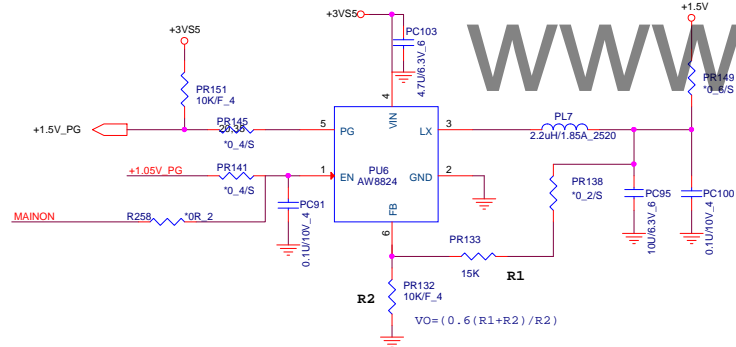


Title			W08
Size	Document Number	+1.0VS5/1.2VS5/1.8VS5	
Custom			Rev A2
Date:	Tuesday, October 08, 2013	Sheet	32 of 38

+1.05V +/- 5%
 Countinue current:1.1A
 Peak current:1.1A
 OCP minimum 2A



+1.5V +/- 5%
 Countinue current:0.6A
 Peak current:1A
 OCP minimum 2A



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Title			W08
Size	Document Number	+1.05V/1.5V	
Custom		Rev	
Date:	Tuesday, October 08, 2013	Sheet	33 of 38

Countinue current:7A
 Peak current:14A
 OCP minimum:16.5A
 Load Line:-5.9mV/A

+VCC_CORE (Bay trail-M 8W)
 Countinue current:6A
 Peak current:12A
 OCP minimum:14A
 Load Line:-5.9mV/A

Place those components close to inductor

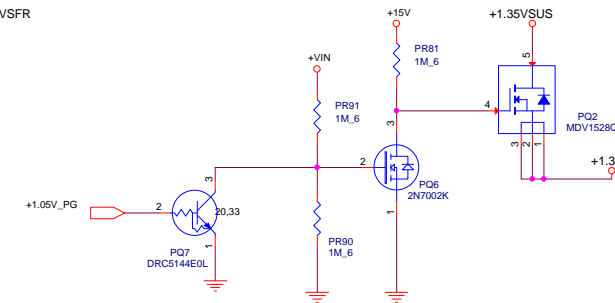
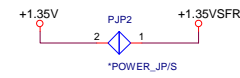
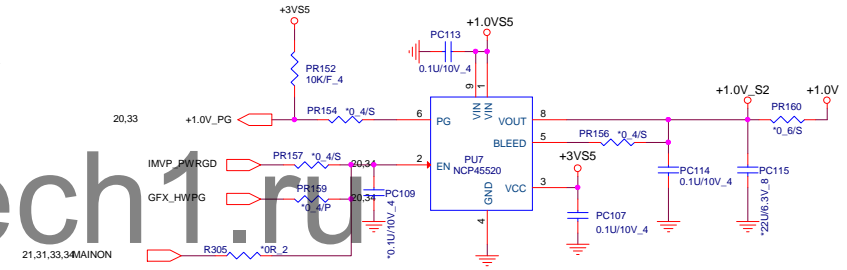
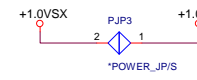
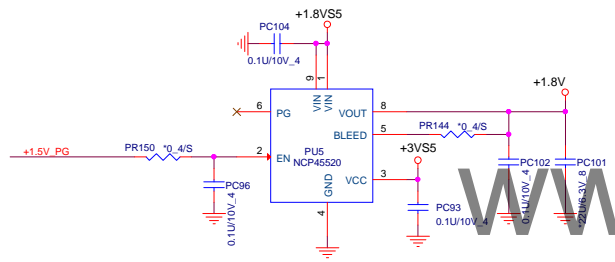
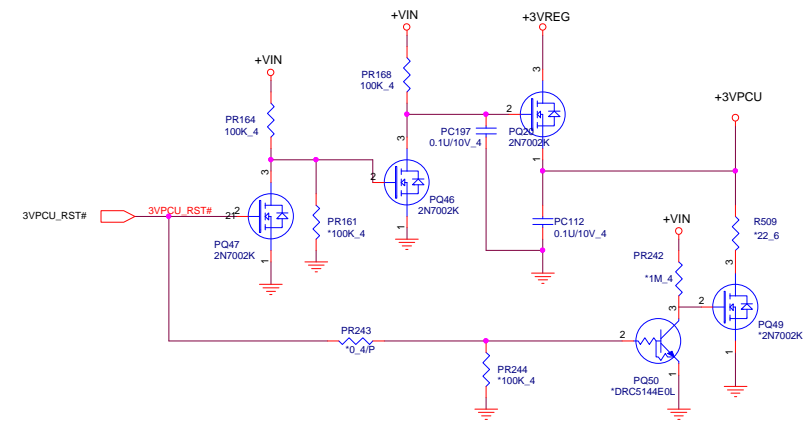
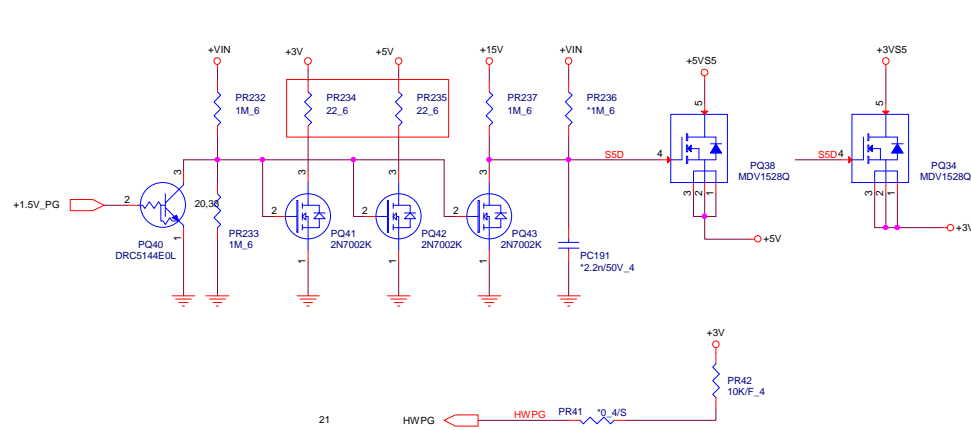
PUT COLSE TO V_GT
 HOT SPOT

PUT COLSE TO V_CORE
 HOT SPOT

File	W08
Size	Document Number
Custom	CPU Core (ISL95833HRTZ-T)
Date	Tuesday, October 08, 2013
Sheet	34 of 38
Rev	A2

Load switch

44

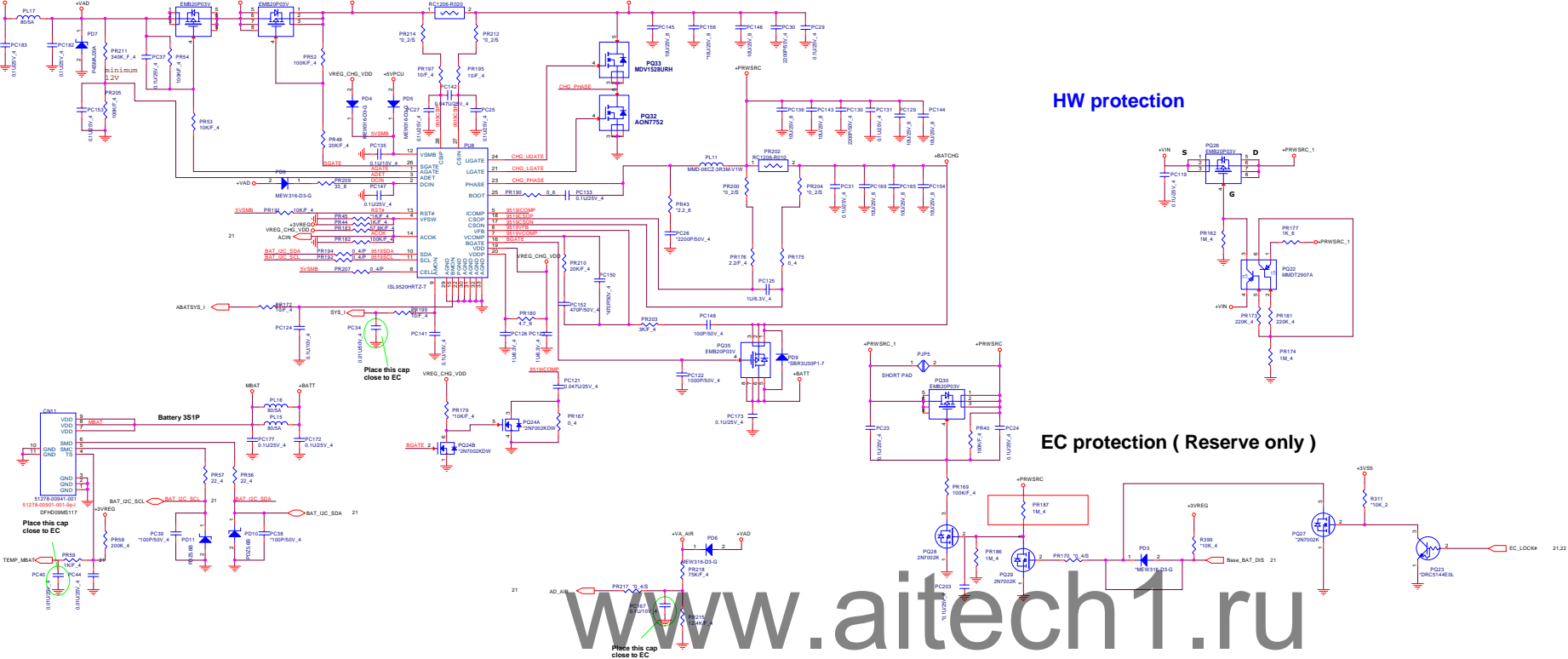


File	W08	Rev	A2
Size	Custom	Document Number	Load switch
Date	Tuesday, October 08, 2013	Sheet	35 of 38



I_SET1	I_SET0	Current Limit
--------	--------	---------------

DC Jack

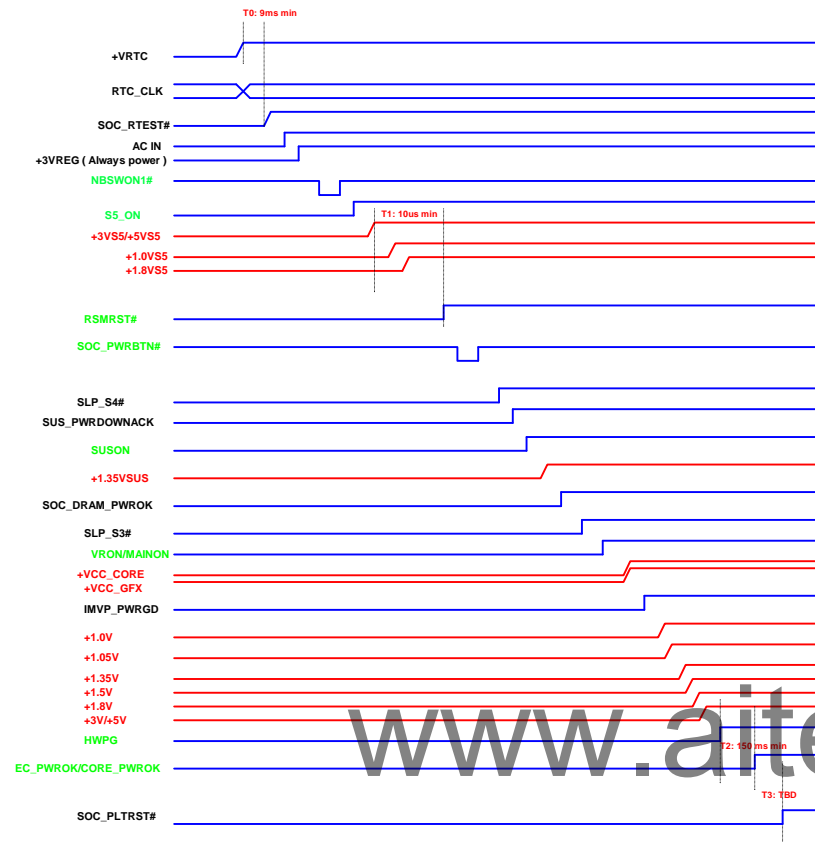


HW protection

EC protection (Reserve only)

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Timing Diagram for G3 to S0



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